Search report

**Reference number:** Lowtech Display

**Note:** Check with team

### **Version 2**

**Cathode ray tube**

The first computer monitors used cathode ray tubes (CRTs). Prior to the advent of home computers in the late 1970s, it was common for a video display terminal (VDT) using a CRT to be physically integrated with a keyboard and other components of the system in a single large chassis. The display was monochrome and far less sharp and detailed than on a modern flat-panel monitor, necessitating the use of relatively large text and severely limiting the amount of information that could be displayed at one time. High-resolution CRT displays were developed for the specialized military, industrial and scientific applications but they were far too costly for general use.Some of the earliest home computers (such as the TRS-80 and Commodore PET) were limited to monochrome CRT displays, but color display capability was already a standard feature of the pioneering Apple II, introduced in 1977, and the specialty of the more graphically sophisticated Atari 800, introduced in 1979. Either computer could be connected to the antenna terminals of an ordinary color TV set or used with a purpose-made CRT color monitor for optimum resolution and color quality. Lagging several years behind, in 1981 IBM introduced the Color Graphics Adapter, which could display four colors with a resolution of 320 x 200 pixels, or it could produce 640 x 200 pixels with two colors. In 1984 IBM introduced the Enhanced Graphics Adapter which was capable of producing 16 colors and had a resolution of 640 x 350.[2]By the end of the 1980s color CRT monitors that could clearly display 1024 x 768 pixels were widely available and increasingly affordable. During the following decade, maximum display resolutions gradually increased and prices continued to fall. CRT technology remained dominant in the PC monitor market into the new millennium partly because it was cheaper to produce and offered to view angles close to 180 degrees.[3] CRTs still offer some image quality advantages[clarification needed] over LCDs but improvements to the latter have made them much less obvious. The dynamic range of early LCD panels was very poor, and although text and other motionless graphics were sharper than on a CRT, an LCD characteristic known as pixel lag caused moving graphics to appear noticeably smeared and blurry.

### **Version 1**

**Cathode ray tube**

The first computer monitors used cathode ray tubes (CRTs). Prior to the advent of home computers in the late 1970s, it was common for a video display terminal (VDT) using a CRT to be physically integrated with a keyboard and other components of the system in a single large chassis. The display was monochrome and far less sharp and detailed than on a modern flat-panel monitor, necessitating the use of relatively large text and severely limiting the amount of information that could be displayed at one time. High-resolution CRT displays were developed for the specialized military, industrial and scientific applications but they were far too costly for general use.Some of the earliest home computers (such as the TRS-80 and Commodore PET) were limited to monochrome CRT displays, but color display capability was already a standard feature of the pioneering Apple II, introduced in 1977, and the specialty of the more graphically sophisticated Atari 800, introduced in 1979. Either computer could be connected to the antenna terminals of an ordinary color TV set or used with a purpose-made CRT color monitor for optimum resolution and color quality. Lagging several years behind, in 1981 IBM introduced the Color Graphics Adapter, which could display four colors with a resolution of 320 x 200 pixels, or it could produce 640 x 200 pixels with two colors. In 1984 IBM introduced the Enhanced Graphics Adapter which was capable of producing 16 colors and had a resolution of 640 x 350.[2]By the end of the 1980s color CRT monitors that could clearly display 1024 x 768 pixels were widely available and increasingly affordable. During the following decade, maximum display resolutions gradually increased and prices continued to fall. CRT technology remained dominant in the PC monitor market into the new millennium partly because it was cheaper to produce and offered to view angles close to 180 degrees.[3] CRTs still offer some image quality advantages[clarification needed] over LCDs but improvements to the latter have made them much less obvious. The dynamic range of early LCD panels was very poor, and although text and other motionless graphics were sharper than on a CRT, an LCD characteristic known as pixel lag caused moving graphics to appear noticeably smeared and blurry.

### Summary

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| --- | --- | --- |
| **Patent number** | **Priorirty date** | **Ranking** |
| US2014232994 | 2011-10-31 | Similar |
| US5038301 | 1987-07-31 | Similar |
| US4775857 | 1985-05-17 | Related |
| WO2005093566 | 2004-03-22 | Related |
| CA1308205 | 1988-08-01 | Related |
| US4709262 | 1985-04-12 | Background |
| US5532763 | 1990-12-27 | Background |

Similar

##### US2014232994 A1

### Low power laser crt and projection system based on parallel flow electron gun

**Abstract:** The present invention relates to electronic technology field, and more particularly to CRT and projection system. A low power laser CRT based on parallel flow electron gun comprises a vacuum tube, a laser panel provided at one end of the vacuum tube and an electron gun provided at the opposing end. The electron gun adopts a parallel flow electron gun, wherein the parallel flow electron gun comprises a negative electrode, a G1 electrode and a control electrode, wherein the control electrode is connected to an electron beam current control system. The electron gun of the present invention adopts parallel flow electron gun to emit electron beam, so that the laser panel has even current density distribution so as to average the power consumption of laser panel to reduce the energy that is converted to heat.

**Passage:** "...system, comprising an light source system, an optical prism group and a projection optical system, wherein the light source system comprises three laser CRTs as recited in claim 1, wherein the colors of laser light source produced by three laser CRTs are three primary colors respectively and the laser produced by three laser CRTs are formed to one three-color synthesized light beam via the optical prism group.20. A projection system, comprising a light source system and a projection optical system, wherein the light source system comprises a laser CRT as recited in claim 1, wherein the laser panel of the laser CTR comprises at least two laser cavities which produce one color of three primary colors, and at least two laser cavities are overlapped in parallel, wherein the laser panel comprises at least two rows of laser cavities, wherein the color of laser emitted from one laser cavity has different color of the laser emitted from the neighboring laser cavity...."

**Claims:** 1. A low power laser CRT based on parallel flow electron gun, comprising: a vacuum tube having a first end and an opposing second end, a laser panel provided at the first end of the vacuum tube and an electron gun provided at the second end, characterized in that the electron gun adopts a parallel flow electron gun, wherein the parallel flow electron gun comprises a negative electrode, a G1 electrode and a control electrode, wherein the control electrode is connected to a electron beam current control system.2. The low power laser CRT based on parallel flow electron gun, as recited in claim 1, wherein the negative electrode and the laser panel are applied with positive voltage respectively,

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**Priority date:** 2011-10-31  
**Inventor:** XIA ZHONGPINGZHAO JIANYAN YUANZHANG XUEYUANZHONG WEIJIE  
**Applicant:** XIA ZHONGPINGZHAO JIANYAN YUANZHANG XUEYUANZHONG WEIJIE  
**CPC class:** H01J29/481G03B21/2033H01J29/48H01J31/10  
**IPC class:** H01J29/48G03B21/20  
**Link:** [Espacenet](http://worldwide.espacenet.com/searchResults?DB=worldwide.espacenet.com&locale=en_EP&query=US2014232994&ST=singleline&compact=false)  
**Legal:** Dead

Similar

##### US5038301 A

### Method and apparatus for multi-monitor adaptation circuit

**Abstract:** A method and apparatus for controlling two or more video display devices using a single display controller, where the display devices generally require different control data. Basic display control parameters are stored in a memory and, when the controller is to be switched from one display to another, the parameters are read from the memory into a substitution device. The substitution device receives modification control signals which depend on the newly selected display device and modifies the display control parameters before re-programming the display controller. The display controller then contains the parameters as appropriately modified for the currently controlled display device. The operator can switch from one monitor to another by generating an appropriate control signal, whereupon the substitution device again reads the control parameters from the memory, modifies them according to the newly selected display device, and re-programs the display controller using the modified parameters.

**Passage:** "...vertical scan rates in the 640.times.350 resolution mode of the enhanced color display and the dual scan monitor are not the same. As a result, a single display controller cannot drive both an enhanced color display and a dual scan monitor in the 640.times.350 resolution mode at the same time. SUMMARY OF THE INVENTION The present invention enables a computer and a single display device, or CRT, controller to drive and control two or more display devices, each of which requires different control parameters. The control parameters for the CRT controller are typically provided from the computer according to software instructions. One embodiment of the present invention utilizes a memory device in conjunction with the CRT controller for storing control parameters which are used to program the CRT controller. Switches are used to indicate to the CRT controller the types of display devices connected to the computer. A control device, utilizing input from the switches, determines..."

**Claims:** What is claimed is: 1. 1. A method for controlling two or more video display devices using a single controller, comprising the steps of: transferring display control data to a memory device and to a substitution device; generating type signals indicative of the types of display devices to be controlled; generating first modification signals as determined by the type of a first display to be controlled; modifying the display control data inn the substitution device in response to the first modification signals; programming the controller using the display control data as modified in response to the first modification signals; generating a switch signal to change control from the

**Publication date:** 1989-02-15  
**Priority date:** 1987-07-31  
**Inventor:** THOMA, III; ROY E.  
**Applicant:** COMPAQ COMPUTER CORPORATION  
**CPC class:** G06F3/1431G09G2360/02  
**IPC class:** G09G5/00G06F3/14G09G1/16  
**Link:** [Espacenet](http://worldwide.espacenet.com/searchResults?DB=worldwide.espacenet.com&locale=en_EP&query=US5038301&ST=singleline&compact=false)  
**Legal:** Alive

Related

##### US4775857 A

### On-line verification of video display generator

**Abstract:** On-line verification apparatus of a display generation system comprises a memory which has a display portion and an inactive display portion, the display portion storing display information, and the inactive display portion storing test data. Scan logic controls a monitor, the scan logic accessing the memory at a predetermined location corresponding to the position control signals. A generator generates display control information to provide information control signals to the scan beam thereby providing the visual display corresponding to the display information stored in the display portion of the memory. A register stores display control information generated from the test data stored in the inactive display portion of the memory during a retrace period. The display control information stored in the register corresponding to the test data is compared to an expected result, during the retrace period, thereby verifying on-line that the display generation system is functioning correctly.

**Passage:** "...I claim: 1. 1. In a video display generator having means for producing color address signals for a RAM color look up memory storing color control signals in addressable storage locations, the color control signals stored in an addressable location of the color look up memory determining the color and intensity of each pixel of a raster scan color CRT scanned by electron beams of the CRT, the pixels of the CRT being arranged in horizontal lines and vertical columns, the line and column number of each pixel constituting each pixel constituting each pixels address; a RAM display memory having addressable memory locations for storing binary data; clock means for producing clock signals, the frequency of which substantially equals the frequency at which each pixel in a given horizontal line of pixels of the array of pixels of the CRT is scanned by the scanning electron beams of the CRT; address counter means to which the clock signals are applied for producing address signals..."

**Claims:** I claim: 1. 1. In a video display generator having means for producing color address signals for a RAM color look up memory storing color control signals in addressable storage locations, the color control signals stored in an addressable location of the color look up memory determining the color and intensity of each pixel of a raster scan color CRT scanned by electron beams of the CRT, the pixels of the CRT being arranged in horizontal lines and vertical columns, the line and column number of each pixel constituting each pixel constituting each pixels address; a RAM display memory having addressable memory locations for storing binary data; clock means for producing clock signals,

**Publication date:** 1988-10-04  
**Priority date:** 1985-05-17  
**Inventor:** STAGGS; KEVIN P.  
**Applicant:** HONEYWELL INC.  
**CPC class:** G09G3/006  
**IPC class:** G09G3/00  
**Link:** [Espacenet](http://worldwide.espacenet.com/searchResults?DB=worldwide.espacenet.com&locale=en_EP&query=US4775857&ST=singleline&compact=false)  
**Legal:** Dead

Related

##### WO2005093566 A1

### Human interface translator for machines

**Abstract:** Apparatus and method of providing, from a video output signal of a machine which supplies video output screens displaying in defined fields text and/or graphics in a first language, corresponding video output screens displaying corresponding text and/or graphics in a selected second language. The video output signal is captured; each screen in the video output signal is identified; each textual fields is identified; for each textual field, the contents are identified; and video output screens are generated corresponding to each of the identified screens, each video output screen having substituted therein corresponding text in the selected second language in place of the original text in the first language.

**Passage:** "...be displayed by the video CRT or LCD monitor. The raster or bit map image on the monitor basically consists of a rectangular grid of&quot;dots&quot;, either monochrome or colored, at various intensities.These dots are known in the computer industry as picture elements, or pixels. The resolution of a monitor reflects the number of pixels in the horizontal and vertical directions, stated as (H x V). There are a number of current and dated video interface standards. Examples of such interfaces include the old IBM monochrome and the IBM CGA, the more recent VGA,SVGA, XVGA and WXVGA. Fundamentally, all of these interfaces share the same underlying operating principle, varying in parameters such as resolution, color space, scanning rate and screen refresh rate.As widely known, the video controller generates a sequence of frames that are then sequentially displayed on the target monitor. The number of sequential frames displayed per second is known as the refresh rate. Typically refresh rates start..."

**Claims:** CLAIMS 1. A method of providing, from a video output signal of a machine which supplies video output screens displaying in one or more defined fields text in a first language, corresponding video output screens displaying in like fields corresponding text in a selected second language, comprising: a. detecting in a screen, of said video output signal, a textual field thereof ; b. for said textual field thereof, identifying textual contents of said field ; and c. generating a video output screen corresponding to the detected screen of the video output signal, said video output screen having substituted therein corresponding text in the selected second language in place of the text in the first

**Publication date:** 2005-10-06  
**Priority date:** 2004-03-22  
**Inventor:** YOUNIS, SAED  
**Applicant:** ALBAHITH CO.YOUNIS, SAED  
**CPC class:** G06K9/325  
**IPC class:** G06F9/44G06F17/28G06K9/32  
**Link:** [Espacenet](http://worldwide.espacenet.com/searchResults?DB=worldwide.espacenet.com&locale=en_EP&query=WO2005093566&ST=singleline&compact=false)  
**Legal:** Dead

Related

##### CA1308205 C

### Method and apparatus for multimonitor adaptation circuit

**Abstract:** METHOD AND APPARATUS FOR MULTI-MONITOR ADAPTATION CIRCUIT A method and apparatus for controlling two or more video display devices using a single display controller, where the display devices generally require different control data. Basic display control parameters are stored in a memory and, when the controller is to be switched from one display device to another, the parameters are read from the memory into a substitution device. The substitution device receives modification control signals which depend on the newly selected display device and modifies the display control parameters before reprogramming the display controller. The display controller then contains the parameters as appropriately modified for the currently controlled display device. The operator can switch from one monitor to another by generating an appropriate control signal, whereupon the substitution device again reads the control parameters from the memory, modifies them according to the newly selected display device, and re-programs the display controller using the modified parameters.

**Passage:** "...and vertical scan rates in the 640 X 350 resolution mode of the enhanced color display and the dual scan monitor are not the same. As a result, a single display controller cannot drive both an enhanced color display and a dual scan monitor in the 640 X 350 resolution mode at the same time. The present invention enables a computer and a single display device, or CRT, controller to drive and control two or more display devices, each of which requires different control parameters. The control parameters for the CRT controller are typically provided from the computer according to software instructions One embodiment of the present invention utilizes a memory device in conjunction with the CRT controller for storing control parameters which are used to program the CRT controller. Switches are used to indicate to the CRT '~ 8%~5 3 7215~-21 controller the types of display devices connec~ed to the computer. A control device, utilizing input from the s~itches, determines the monitor type which..."

**Claims:** 1. A method for controlling two or more video display devices using a single controller, comprising the steps of: transferring display control data to a memory device and to a substitution device; generating type signals indicative of the types of display devices to he controlled; generating first modification signals as determined by the type of a first display to be controlled; modifying the display control data in the substitution device in response to the first modification signals; programming the controller using the display control data as modified in response to the first modification signals; generating a switch signal to change control from the first display device to a second display

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**Priority date:** 1988-08-01  
**Inventor:** THOMA, ROY E., III  
**Applicant:** COMPAQ COMPUTER CORPORATION  
**IPC class:** G06F3/12  
**Link:** [Espacenet](http://worldwide.espacenet.com/searchResults?DB=worldwide.espacenet.com&locale=en_EP&query=CA1308205&ST=singleline&compact=false)  
**Legal:** Dead

Background

##### US4709262 A

### Color monitor with improved color accuracy and current sensor

**Abstract:** A color monitor having a highly accurate video processing circuit for providing a high accuracy color CRT display is disclosed which utilizes three feedback loops in each of the three primary color channels, each feedback loop including both the final video amplifier and the CRT in order to achieve equalization and stabilization of their combined signal-to-brightness transfer characteristics against both CRT and circuit drift. The video processor circuitry also includes a D/A converter for generating a signal representative of the desired amplitude component for each primary color from a three bit digital input. By utilizing the three feedback loops, the color produced in the CRT display is independent of the operator set brightness and contrast settings and is maintained to within 10% of its desired value over varying temperature and adverse environmental conditions.

**Passage:** "...the representative signal and applying it to the cathode of said CRT. 9. 9. The video processing system of claim 7, wherein said digital-to-analog converter converts a three bit digital color code input to a signal representative of a desired amplitude component for a primary color of said CRT. 10. 10. The color video monitor of claim 6, wherein a plurality of said means for processing an input video signal is utilized. 11. 11. The color video monitor of claim 10, wherein each of said plurality of said means for processing an input signal independently processes a different primary color of said CRT. 12. 12. The video processing system of claim 6, wherein said means for producing, said white sample level circuit means, said black sample level circuit means and said cathode stabilization circuit means form a one-color video processing subsystem and wherein a plurality of said video processing subsystems is utilized, one for each of the primary colors of said CRT. 13...."

**Claims:** What is claimed is: 1. 1. A color video monitor having a video siganl path for providing an input video signal and for displaying the video signal on a high resolution CRT display, comprising: means for processing an input video signal, said means for processing associated with the video signal path such that the video signal is not affected by said means for processing; means for producing color convergence signals; first means, connected to receive said color convergence signals, for providing vertical deflection signals and convergence driver signals for said color video monitor; second means, connected to the first means, for providing horizontal deflection signals and dynamic

**Publication date:** 1987-11-24  
**Priority date:** 1985-04-12  
**Inventor:** SPIETH; ROBERT H.DELORENZO; ANTHONY N.  
**Applicant:** HAZELTINE CORPORATION  
**CPC class:** G09G1/285  
**IPC class:** G09G1/28  
**Link:** [Espacenet](http://worldwide.espacenet.com/searchResults?DB=worldwide.espacenet.com&locale=en_EP&query=US4709262&ST=singleline&compact=false)  
**Legal:** Alive

Background

##### US5532763 A

### Single panel color projection video display

**Abstract:** A color projection video system utilizing only a single light valve. A white light source is separated into into red, green and blue bands. Scanning optics cause the RGB bands to be sequentially scanned across a light valve, such as a transmission LCD panel. Prior to each color passing over a given row of panels on the light valve, that row will be addressed, by the display electronics with the appropriate color content of that portion of the image which is being displayed. The image is projected by a projection lens onto a viewing surface, such as a screen. The sequence of light bands occurs so quickly as to give the viewer an appearance of simultaneous full color.

**Passage:** "...BACKGROUND OF THE INVENTION This invention relates to color video projection systems and particularly to a single light valve panel color projection display. Projection television (PTV) and video color display systems, especially rear projection display systems, are a popular way to produce large screen displays, i.e. picture diagonal of 40 inches or greater, as the projection method provides displays which are lighter, cheaper, and in many cases, superior in brightness and contrast, than non-projection based displays. Direct view cathode ray tube (CRT) based systems still dominate non-projection display technology, especially for, 9 inch to 30 inch color displays. In unit and dollar volume, the major market for all such displays is the consumer market. Size, cost, brightness, contrast and to a lesser extent, resolution are important characteristics of consumer designs. Because large direct view CRT based displays are heavier, bulkier, and more expensive, projection consumer..."

**Claims:** What is claimed is: 1. 1. A color display system comprising: (a) a light valve having an array of rows of addressable pixels for modulating light impinging on the light valve in accordance with display signals applied to the pixels; (b) means for providing at least two light beams, each beam having a different color, and each beam being wider in the direction of the width of the pixel rows to be addressed and narrower in the direction of the height of the pixel rows to be addressed; (c) means for repeatedly sweeping the beams sequentially across a surface of the light valve in the height direction in a manner that each of the at least two different color beams illuminates only a portion

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**Priority date:** 1990-12-27  
**Inventor:** JANSSEN; PETER J.BRADLEY; RALPH H.BINGHAM; JOSEPH P.GUERINOT; WILLIAM F.OTTO; DETLEV  
**Applicant:** NORTH AMERICAN PHILIPS CORPORATION  
**CPC class:** H04N9/3117G02F2001/133622G09G3/34G09G3/3607G09G2310/0235G09G2310/0297  
**IPC class:** G03B21/00G02F1/13357G09G3/34G09G3/36H04N9/31  
**Link:** [Espacenet](http://worldwide.espacenet.com/searchResults?DB=worldwide.espacenet.com&locale=en_EP&query=US5532763&ST=singleline&compact=false)  
**Legal:** Dead